

Dr. Parveen



Nationality: Indian
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Education Qualifications

Ph.D. in Electronic Science From Delhi University in 2016

Title of Thesis: *Three-Port RF Characterizations and Noise Modeling of Separate Gate Double Heterostructure InAlAs/InGaAs/InAlAs DGHEMT for Applications for Millimeter wave and Mixer Application*

June 2010 **UGC-NET June'10 (JRF)** in Electronic Science

2007 to 2009 **MSc. Electronics**
Department of Electronic Science
Delhi University

2004 to 2007 **B.Sc.(H) Electronics**
A.R.S.D College
Delhi University, New Delhi

Work Experience

Microelectronics Research Lab,
Department of Electronic Science,
A.R.S.D College,
University of Delhi South Campus, New Delhi

Project Fellow in Major Research Project

April 2010 to January 2011

Teaching Experience

- Guest Faculty in A.R.S.D, College, Delhi University, From 11th January 2016 to 26th April 2016,
- Guest Faculty in I P College of Women, Delhi University, From August 2016 to November 2016
- Assistant professor (Ad-Hoc) in Zakir Hussian Delhi College, Delhi University, From 10th August 2017 to May 2017
- Guest Faculty in Cluster Innovation Centre (CIC) , College, Delhi University, From September 2017 to till date ,
- Guest Faculty in Zakir Hussian Delhi College, Delhi University, From February 2018 to till date

Project

- “**Design of Serial Communication Between PC and Microcontroller 8051**” under the Guidance of **Mr. Raguvendra Scientist F, National Physical Laboratory**, Pusa Road New Delhi
- **Fabrication and Characterization of ZnO base Dye-Sensitized Solar Cell**

Research Publications

▪ *Publications in Referred/Peer Reviewed International Journals*

- 1) Neha Verma, ***Parveen***, Jyotika Jogi, “Quantum Simulation of a Double Gate Double Heterostructure InAlAs/InGaAs HEMT to Analyze Temperature Effects”, *International Journal of Simulation, Systems, Science and Technology (IJSSST)*, vol. 16, No. 3, pp. 10.1-10.8, 2015. doi: DOI 10.5013/IJSSST.a.16.10, ISSN: ISSN: 1473-804x online, 1473-8031 print
- 2) ***Parveen***, Monika Bhattacharya, Jyotika Jogi, “Modeling of InAlAs/InGaAs/InAlAs DG-HEMT Mixer for Microwave Application”, *International Organization of Scientific Research, Journal of Electrical and Electronics Engineering (IOSR-JECE)*, vol. 10, Issue 4 (version I), pp. 20-29, Jul-Aug. 2015, e-ISSN: 2278-1676, p-ISSN: 2320-3331, Impact Factor: 1.452.
- 3) ***Parveen***, Monika Bhattacharya, Jyotika Jogi, “Evaluation of Noise Coefficients for Separate Gate InAlAs/InGaAs Double Heterostructure DG-HEMT”, *International Journal of Engineering Research and Development (IJERD)*, Volume 10, Issue 8 (August 2014), PP.01-11, e-ISSN: 2278-067X, p-ISSN: 2278-800X, Impact Factor: 1.43
- 4) ***Parveen***, Mridula Gupta, R.S. Gupta, and Jyotika Jogi, "RF Characterization of 100nm

Separate Gate InAlAs/InGaAs DG-HEMT”, *Microwave Optical Technology Letters (MOTL)*, Vol. 55, No. 11, pp. 2796-2803, November 2013. [DOI:10.1002/mop.27935](https://doi.org/10.1002/mop.27935), ISSN:1098-2760, Impact Factor: 0.623

▪ ***Publications in International Conference Proceeding***

- 1) ***Parveen***, Neetika Sharma, Jyotika Jogi* “COMPREHENSIVE NOISE PERFORMANCE ASSESSMENT OF SEPARATE GATE CONTROL IN InAlAs/InGaAs/InAlAs DOUBLE GATE HEMT” International Conference On Advances in Nanomaterials and Nanotechnology (ICANN-2016), 4th to 5th November 2016 New Delhi, Delhi, India
- 2) ***Parveen***, Neha Verma, Monika Bhattacharya, and Jyotika Jogi, “A Novel Separate Gate InAlAs/InGaAs/InAlAs DG-HEMT Heterogeneous Mixer, IEEE Conference Tencon 2015, 1st – 4th November, Macau.
- 3) Neha Verma, ***Parveen***, and Jyotika Jogi, “Quantum Simulation of a Double Gate Double Heterostructure InAlAs/InGaAs HEMT to Analyze Temperature Effects”, IEEE Conference, 17th UKSIM-AMSS International Conference on Modelling and Simulation, March.25-27, 2015, Cambridge University, UK, ISBN: 978-1-4799-8713-9, [DOI 10.1109/UKSim.2015.39](https://doi.org/10.1109/UKSim.2015.39)
- 4) ***Parveen***, Neha Verma, and Jyotika Jogi, “Impact of Doping Concentration and Temperature variation on the Noise Performance of Separate Gate InAlAs/InGaAs DG-HEMT”, IEEE Conference TENCON-14, Bangkok, Thailand, Oct.22-25, 2014, ISSN: 2159-3442, Print ISBN: 978-1-4799-4076-9, [DOI: 10.1109/TENCON.2014.7022339](https://doi.org/10.1109/TENCON.2014.7022339).
- 5) Neha Verma, ***Parveen***, and Jyotika Jogi, “Effect of Variation in Channel Thickness on Eigenenergies of Double Triangular Quantum Well in Double Gate InAlAs/InGaAs HEMT”, IEEE Conference TENCON-14, Bangkok, Thailand, Oct.22-25, 2014, ISSN: 2159-3442, Print ISBN: 978-1-4799-4076-9, [DOI: 10.1109/TENCON.2014.7022286](https://doi.org/10.1109/TENCON.2014.7022286) (*Received Best Paper Award*)
- 6) ***Parveen***, Mridula Gupta, R.S. Gupta, and Jyotika Jogi, “Intrinsic Admittance Parameter For Separate Gate InAlAs/InGaAs DG-HEMT For 100 nm Gate length”, IEEE ICT 2013, IEEE Conference on Information and Communication Technologies, pp. 750-754, April 11-12, 2013, Noorul Islam University, Thuckalay, Tamil Nadu, India, p-ISSN:978-1-4673-5759-3, [DOI: 10.1109/CICT.2013.6558194](https://doi.org/10.1109/CICT.2013.6558194)
- 7) Neha Verma, ***Parveen***, and Jyotika Jogi, “Quantum Simulation for Separate Double Gate InAlAs/InGaAs HEMT”, IEEE ICT 2013, IEEE Conference on Information and Communication Technologies, pp. 765-769, April 11-12, 2013, Noorul Islam University, Thuckalay, Tamil Nadu, India, p-ISSN:978-1-4673-5759-3, [DOI: 10.1109/CICT.2013.6558197](https://doi.org/10.1109/CICT.2013.6558197).
- 8) ***Parveen***, Sweety Supriya, Dushyant Gupta, and Jyotika Jogi, “A Novel Analytical Model for Small Signal Parameter for Separate Gate InAlAs/InGaAs DG-HEMT”, IEEE Conference TENCON-12, Philippines, Cebu, Nov.19-21, 2012, IEEE Xplore, e-ISSN: 978-1-4673-4822-5, p-ISBN: 978-1-4673-4823-2 [DOI: http://dx.doi.org/10.1109/TENCON.2012.6412183](https://doi.org/10.1109/TENCON.2012.6412183).
- 9) ***Parveen***, Mridula Gupta, R.S Gupta and Jyotika Jogi, “Scattering Parameter for Separate Gate InAlAs/InGaAs/InP based DG-HEMT for 100nm Gate Length”, NANOCON 012

(2nd international conference on nanotechnology) Conference Proceedings, pp.1144-1154,18th-19th October 2012, Pune, India.

- 10) ***Parveen***, Mridula Gupta, R.S. Gupta, and Jyotika Jogi, “Analytical modeling of Enhanced Performance of Separate Gate In_{0.52}Al_{0.48}As-In_{0.53}Ga_{0.47}As DG-HEMT for Nanometer Gate Dimension”, International Conference on Microwave, Antenna and Remote Sensing (ICMARS) Jodhpur, pp. 162-164, Rajasthan, India, Dec. 07-09, 2011.
- 11) ***Parveen***, R.S. Gupta, Mridula Gupta and Jyotika Jogi, “Analytical modeling of the dynamic performance of In_{0.52}Al_{0.48}As-In_{0.53}Ga_{0.47}As Separate gate DG-HEMT for nanometer gate dimension”, International Conference on microwave and Optical technology (ISMOT), Prague, Czech Republic, EU, June 20-23, 2011. ISBN-978-80-01-04887-0

Seminars and Workshops

- Hands-on Workshop on PCB Fabrication, Department of Electronics, Maharaja Agrasen College, Delhi University, 2nd April 2017
- Faculty Development Workshop On Embedded Systems and Synthesis of Nano-materials JANUARY 6th -7th, 2017 under the aegis of DBT - star college scheme Organized by DEPARTMENT OF PHYSICS & ELECTRONICS & IQAC, Hansraj College, Delhi University
- The 18th WIMNACT-MQ3- New Delhi- Workshop Mini-Colloquia on Nanometer CMOS Technology, Mini-Colloquia On Compact Modeling and Fabrication Techniques of Techniques of Advance MOSFET/HEMT Structure” University of Delhi South Campus, 14th -15th March, 2012.
- Second National Workshop on Quantum Mechanics: Theory and Application at Deen Dayal Upadhyaya College, 29th -30th October, 2010.
- Workshop on Scilab and its Applications at Acharya Narendra Dev College, University of Delhi, 10th -11th March, 2011.
- Mini Colloquia on Compact Modeling Techniques for Nanoscale Devices and Circuit Analysis at University of Delhi South Campus, 14-15 March, 2012.
- Seminar on Recent Advances in Microwave and Photonic Devices at Maharaja Agrasen Institute of Technology, 4th October, 2012.

International Conference Attended

- NCRDE 2017, Department of Electronic, University of Delhi South Campus
- ICANN Conference, Delhi, India, 2016
- IEEE Conference TENCON-15, Macau, China, 2015

- IEEE Conference TENCON-14, **Bangkok, Thailand, 2014**
- IEEE Conference on Information and Communication Technologies (ICT) Noorul Islam University, Thuckalay, **Tamil Nadu, India, 2013**
- Nanocon012(International conference),**Pune, India,2012**
- International Conference on Microwave, Antenna and Remote Sensing (ICMARS) **Jodhpur, India 2011**

Achievements and Interests

- Passed an online test conducted during the Scilab workshop (workshop funded by NME-ICT, MHRD through IIT Mumbai).
- Received best paper award in an IEEE conference (IEEE TENCON 2014) held at Bangkok, Thailand in 2014.
- Graduate student member of IEEE (membersip no. 92411603).
- Knowledge of Device Simulation software ATLAS by SILVACO, VTCAD, and other software like ,Visual basic for C and C++, Netbean for Java, Eagle for PCB fabrication, P-Spise for circuit simulation, VHDL, MATHCAD, Scilab, Mathematica, Matllab, LabVIEW

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