

**Amit Kumar Singh**

Assistant Professor, Electronics Engineering,  
Cluster Innovation Centre,  
University of Delhi

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**Education Details**

M.Tech. (VLSI Systems and Technology), SOE, Shiv Nadar University, Gautam Budh Nagar, 2014

B.Tech. (Electronics and Telecommunication Engineering), FET, SRM University, Kattankulathur, 2010

**Professional Experience**

Assistant Professor, Cluster Innovation Centre, University of Delhi, August 2014-Present

Teaching Associate, Shiv Nadar University, Gautam Budh Nagar, August 2012-May 2014

Project Engineer, Wipro Technologies Pvt. Ltd. Bangalore, August 2010-March 2012

**Research and Teaching Interests**

Digital Signal Processing, Speech Processing, Signals and Systems, VLSI Technology & Design

Circuit Simulation using LT SPICE, Cadence, MentorGraphics, Tanner, Signal Processing with Matlab

**Publications**

Amit Kumar Singh, Rohit Singh, Ashutosh Dwivedi, Mel Frequency Cepstral Coefficients based Text Independent Automatic Speaker Recognition using Matlab, In Optimization, Reliability and Information Technology (ICROIT), 2014 International Conference on, pp. 524-527. IEEE, 2014

Amit Kumar Singh, Rohit Singh, Ashutosh Dwivedi, Evolvement and Recent Research in Parametric Representations of Speech Features for Automatic Speaker Recognition, International Journal of Electrical, Electronics and Data Communication, Volume-2, Issue-1, pp 11-15, January 2014.

Sonal Singhal, Rohit Singh, Amit Kumar Singh, Design of a Sub-0.4 V Reference Circuit in 0.18 $\mu$ m CMOS Technology, Advanced Materials Research, Manufacturing Science and Technology (ICMST2013), Vols. 816-817, pp 882-886, Trans Tech Publications, Switzerland, 2013.

Rohit Singh, Sonal Singhal, Amit Kumar Singh, Design and Optimization of a Low Power Voltage Reference Generator Circuit in 45nm CMOS Technology, International Journal of Innovative Research and Development(IJIRD), Volume 3, Issue 1, pp 298-304, January 2014.

Dwivedi Praveen, Amit Kumar Singh and R.G. Sangeetha, A Comparative Analysis of Transimpedance Amplifier in Giga-bit Optical Communication, Research Journal of Engineering Sciences (International Science Congress Association -ISCA-RJEngS), March-2014.

Md Arif Khan, Amit Kumar Singh, Ashutosh Dwivedi, Educational Graphical User Interface for nMOS Models, International Journal of Electrical & Electronics Engineering Advanced Research (IJEEEAR), Vol. 2, Issue 2, pp-160-165, February 2014.

Sunil Kumar Niranjana, Amit Kumar Singh, Ashutosh Dwivedi, Design of a Threshold Voltage Adjustable Digital Buffer, International Journal of Electrical & Electronics Engineering Advanced Research (IJEEEAR), Vol. 2, Issue 2, pp- 165-167, February 2014.