

Practical Examination Evaluation Assignment

Program: B. Tech. (It & Mathematical Innovation)

Department: Cluster Innovation Centre, University of Delhi

Semester: IV

Title of Paper: Digital Electronics and Logic Design Innovation

Paper code: (911416)

Name of Student:

Roll Number of student:.....

Date of Submission:.....

Instructions:

- a. Email your solved assignment to pankajtyagicicdu@gmail.com or whatsapp at 9810726750 on or before 20.05.2020.
- b. Mention the name of the Program, Semester, title of practical paper and name of the student at the top of each of the answer sheets before submission.

Answer the following question:

1. Explain realization of logic gates through diodes and resistors.
2. Give design of a shift register using flip-flops.

OR

Give design of full adder and subtractor circuit.

3. Explain in brief the Innovation Project you decided to work upon and the work done on it.